

Patent

Attorney Docket No.: Intel 2207/10122
Assignee: Intel CorporationIN THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANTS : Sailesh B. KOTTAPALLI

SERIAL NO. : 09/753,764

FILED : December 29, 2000

FOR : METHOD AND APPARATUS FOR INSTRUCTION
POINTER STORAGE ELEMENT CONFIGURATION IN A
SIMULTANEOUS MULTITHREADED PROCESSOR

GROUP ART UNIT : 2183

EXAMINER : David J. HUISMAN

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VIA FACSIMILE

M/S: AF
Commissioner for Patents
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CERTIFICATE OF MAILING

I hereby certify that this correspondence is being facsimile transmitted to the United States Patent and Trademark Office via facsimile number: (571) 273-3300 or deposited with the United States Postal Service as first class mail in an envelope addressed to: M/S: AF, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on *Barbara Vance*

Dated: August 15, 2005

Barbara Vance

NOTICE OF APPEAL

Dear Sir:

Rejected claims 1-22, in the decision of the Examiner dated May 13, 2005, are hereby appealed to the Board of Patent Appeals and Interferences. Please charge payment of the appeal fee of \$500.00 to Deposit Account No. 11-0600.

The Commissioner is hereby authorized to charge payment of any additional fees and/or patent application processing fees required under 37 C.F.R. §1.16 and §1.17 or credit any overpayment to Deposit Account No. 11-0600.

Respectfully submitted,

KENYON & KENYON

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Dated: August 15, 2005

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